

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF:	)
	)
TOMOHARU TANAKA ET AL.	) GROUP ART UNIT: 2133
	)
SERIAL NO.: UNASSIGNED	) EXAMINER: P. CHUNG
DIV. OF SERIAL NO.: 09/604,692	)
	)
FILED: HEREWITH	)
	) ATTY. DKT. NO. 01701.00190
FOR: FLASH MEMORY	)

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with Applicants' duty of disclosure, the enclosed information is submitted to the United States Patent and Trademark Office in connection with the above-identified application. The information is identified on the attached PTO Form-1449.

This application relies, under 35 U.S.C. §120, on the earlier filing date of prior U.S. Application Serial No. 09/604,692 filed on June 27, 2000. The references identified on the attached Form PTO 1449 were submitted to and/or cited by the Patent and Trademark Office in these prior applications and, therefore, copies are not required to be provided in this application. See 37 C.F.R. Section 1.98(d).

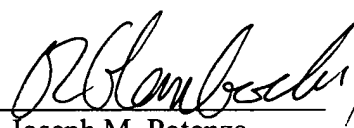
Applicant does not waive any right to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application.

TOMO HARU TANAKA ET AL. (Divisional of U.S. Serial No. 09/604,692)

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed Form PTO-1449 in the appropriate place to indicate that the document has been considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

Respectfully submitted,

By:

  
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Dated: June 24, 2003

PTO-1449 (Modified)  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. 01701.00190	SERIAL NUMBER Div. of 09/604,692
	APPLICANT Tomoharu Tanaka et al.	
	FILING DATE Herewith	GROUP ART UNIT 2133

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	4,646,312	02/1987	Goldsbury et al.			
	4,706,249	11/1987	Nakagawa et al.			
	5,337,317	08/1994	Takamisawa et al.			
	5,621,682	04/1997	Tanzawa et al.			
	5,719,888	02/1998	Tanzawa et al.			
	5,793,724	08/1998	Ichikawa et al.			
	5,603,001	02/1997	Sukegawa et al.			
	5,864,569	01/1999	Roohparvar			
	5,901,152	05/1999	Tanaka et al.			
	5,933,436	08/1999	Tanzawa et al.			
	5,996,108	11/1999	Tanzawa et al.			

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO	
	98 33112	7/1998	WIPO				
	0 509 485 A1	12/1992	United Kingdom			X	
	96/29704	09/1996	WO			X	
	0 802 540 A2	04/1997	Europe			X	
	98/10425	03/1998	WO			X	
	98/33112	07/1998	Japan			X	

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Toru Tanzawa et al., "A Compact On-Chip ECC for Low Cost Flash Memories", IEEE Journal of Solid-State Circuits, Vol. 32, No. 5, May 1997.

EXAMINER	DATE CONSIDERED
EXAMINER: Initial citation if reference was considered. Draw line through citation if not in conformance to MPEP 609 and not considered. Include copy of this form with next communication to applicant.	